Keywords: Component, reusability, DEVS, COM

Abstract
This paper applies a component-based framework to discrete event systems simulation and then develops a component-based simulation environment. The environment is based on combination of the sound modeling formalism of DEVS (Discrete Event Systems Specification) and the powerful component standard of COM (Component Object Model). The combination results in the DEVS/COM run-time infrastructure which supports binary reusability of simulation models developed with multi-languages in a modular form. It has been shown that such reusability provides modelers with flexibility in modeling as well as efficiency in models development time.

Introduction
The component-based framework, proposed by market, is a methodology to enable reusability on various development stages and discrete event simulation is a powerful tool for system design and the DEVS (Discrete Event System Specification) formalism proposed by Zeigler [1] provides sound formal modeling and simulation framework for the generic system.

There are several approaches each of which enables a DEVS (Discrete Event System Specification) framework to a component capability. The first approach is based on a Java component technology [2], which implements a visual modeling and simulation environment. The second approach is DEVS/CORBA execution environment [3] that focused on the distributed simulation of the network-friendliness of the component technology.

Both DEVS-JavaBean and DEVS/CORBA frameworks utilize the potential of component technology and their feature is strongly concerned with the component technology that each framework adopted. In this paper a suggested component-based simulation environment is based on the Microsoft’s COM run-time infrastructure. COM is currently the strongest competitor on the component technology and it is a sound framework to interoperate under the many co-existing different programming language as a reusable binary component and its network friendliness may provide further extensibility for an application to be extended to a distributed environment. Combining the advantages of DEVS and COM may provide heterogeneous and reusable modeling and simulation environment.

In this paper, DEVS/COM component simulation environment is suggested to meet component reusability requirement. To set the stage the brief overview on the component technology and model reusability is provided. We then proceed to the design of DEVS/COM simulation environment with simple banking example. Some discussion about overall reusability will be followed.

COMPONENT TECHNOLOGY AND SIMULATION MODELS REUSABILITY

Component Technology and Supporting Runtime Infrastructures
Traditional software development requires application executables to be compiled and linked with their dependencies. Every time a developer wants to use a different processing logic or new capabilities, he or she needs to modify and recompile the primary application to support them. To manage with the rapid model change, the reuse of system components is strongly required to improve the software development productivity and quality.

Software components are binary units that are composed without modification [4]. Even though different definitions about the components are found on every other literature, a software component must meet the following properties

1. A component is a unit of independent deployment
2. A component is a unit of third-party composition
3. A component has no persistent state

These properties mean that a software component is a unit of a reusable piece of software without any knowledge on the internal structure.

There are several component technologies that compete to gain the name of the standards. The three important players for component world are COM (Component Object
Model) proposed by Microsoft, JavaBean by Sun and CORBA (Common Object Request Broker Architecture) by OMG (Object Management Group). The each component approach are summarized in the following table:

<table>
<thead>
<tr>
<th>Platform</th>
<th>COM</th>
<th>CORBA</th>
<th>JavaBeans</th>
</tr>
</thead>
<tbody>
<tr>
<td>Programming Language</td>
<td>Any</td>
<td>Any</td>
<td>Java</td>
</tr>
<tr>
<td>Approach to component</td>
<td>Application data exchange format</td>
<td>Distributed environment</td>
<td>Programming language</td>
</tr>
</tbody>
</table>

The benefits from the component software, as embodied in the COM, are as follows: [5]
1. Abstraction and black-box encapsulation
2. Extensibility
3. Reusability
4. Cross language
5. Location independence

Models Reusability in Component-Based Modeling/Simulation

A number of paradigms and programming methodologies have been offered to develop reusable and extensible code. One such paradigm, object-oriented methodology, has been received favorably in general. Under this paradigm, certain sets of related features are grouped in a single unit called an object and it creates an abstraction layer that simplifies many systems.

On the traditional procedure for modeling, the reusability is achieved by obtaining the part of the source code of the concern. On some case reusability is achieved by sharing some of the program module in a library form and program language-specific header file. With the innovation of the object-oriented paradigm, some aspects of the system can be considered as an object to be reused and that results in great reduction of source code [6]. But as the increasing size of the overall system and the number of the system designer involved, it is harder to knit each module to make a whole picture. In the object oriented development procedure, change on some object may propagate to other object and the overall system must be built in a synchronous fashion to avoid this problem.

The component-based development framework overcomes this development in large problem by immutable interface—a contract with other component cannot be modified- and binary form of component. Binary form provides true information hiding on the internal structure of the component that results in true black-box encapsulation and makes it possible for any programming language to make a component – language independence.

The full feature of the object-oriented modeling framework can be used in the component-based system designing and a proposed simulation model can be easily distributed as a form of a component. While reusing an object in the object-orient framework still applies in the component inside, the component model itself can be reused in a simple Lego-block way.

DEVS/COM ENVIRONMENT

A brief overview on the DEVS formalism is introduced in the first section and architecture and implementation of the DEVS/COM environment will be followed. Some capability on the cross language model implementation is discussed on the last section.

DEVS Formalism: A Brief Overview

The DEVS (Discrete Event System Specification) formalism introduced by Zeigler is a set-theoretic formalism and it provides a means of modeling discrete event system in a hierarchical, modular way. With this DEVS formalism, we can perform modeling more easily and correctly by decomposing large system into component models and specify the coupling between them. There are two kinds of model in DEVS formalism.

Atomic model is the basic model and has specification for the dynamics of the model. It describes the behavior of a component, which is indivisible, in a timed state transition level. Formally, a 7-tuple specifies an atomic model M as follows

\[ AM = \langle X, Y, S, \delta_{int}, \delta_{ext}, \lambda, ta \rangle \]

Where

\[ X : \text{input events set} \]
\[ Y : \text{output event set} \]
\[ S : \text{sequential states set} \]
\[ \delta_{int} : S \rightarrow S \text{ is the internal transition function} \]
\[ \delta_{ext} : Q \times X \rightarrow Q \text{ is the external transition function, where } Q = \{(s,e)|s \in S, 0 \leq e \leq \text{last transition} \} \]
\[ \lambda : S \rightarrow Y \text{ is the output function} \]
\[ ta : S \rightarrow R^+ \text{ is the time advance function, where } R^+ = \{x | x \geq 0 \} \]

The four elements in the 7-tuple, namely \( \delta_{int}, \delta_{ext}, \lambda \) and \( ta \), are called characteristic functions.

The second form of the model, called a coupled model (or coupled DEVS), tells how to couple several component models together to form a new model. This kind of model can be employed as a component in a larger coupled model.
thus giving rise to the construction of complex models in a hierarchical fashion. The DEVS coupled model is defined as follows.

\[
CM = < X, Y, M, EIC, EOC, IC, SELECT >
\]

Where

- \(X\) : input events set
- \(Y\) : output events set
- \(M\) : set of all component models in DEVS
- \(EIC \subseteq X \times \cup_i X_i\) : external input coupling relation
- \(EOC \subseteq \cup_j Y_i \times Y\) : external output coupling relation
- \(IC \subseteq \cup_i X_i \times \cup_j Y_i\) : internal coupling relation
- \(SELECT : 2^M - \phi\) \(M\) is a function which chooses one model when more than 2 models are scheduled simultaneously.

Architecture of DEVS/COM environment

The Component Object Model (COM) is a Microsoft’s component software architecture that allows applications and systems to be built from components supplied by different software vendors. COM specifies a component interface definition, which is independent from programming languages, such as C, C++, Java, and Visual Basic.

On the various reusability approaches like component composition and code generation, both DEVS framework and COM runtime infrastructure utilize component composition way. The hierarchical and modular specification of the DEVS formalism and the binary component of the COM component can be onto correspondence. That is, DEVS abstract simulator (Figure 1) and user model is directly correlated to a COM component, as there is no alteration on the simulation algorithm or message structure.

![Figure 1. DEVS abstract simulator](image)

DEVS Models Implementation in DEVS/COM

As component DEVS user model also is a component, an implementation of the user model is somewhat sophisticated and it can be some overhead to modeler. Like the inheritance of the object-oriented framework, COM supports a reusability and extensibility by a kind of binary inheritance called containment and aggregation even though COM does not support strict-sense implementation inheritance.

A basic skeleton model is provided to reduce the modeling load and modeler use the basic model by either containment or aggregation. Functions by basic skeleton model for the DEVS atomic and coupled model are as followed (Table 2, Table 3). The function names are not changed in the comparison with DEVS object oriented simulator. Actually, as DEVS/COM simulator communicates with user model by interface, these API’s are the interfaces provided by DEVS/COM simulator.

<table>
<thead>
<tr>
<th>Table 2. Component API’s for Atomic Model</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Atomic Model</strong></td>
</tr>
<tr>
<td>Function</td>
</tr>
<tr>
<td>X AddInport([in] BSTR input)</td>
</tr>
<tr>
<td>Y AddOutport([in] BSTR output)</td>
</tr>
<tr>
<td>S</td>
</tr>
<tr>
<td>(\delta_{ext}) ExtTransFn([in] double e, [in] Messages msg)</td>
</tr>
<tr>
<td>(\delta_{int}) IntTransFn()</td>
</tr>
<tr>
<td>(\lambda) OutputFn([in] IMessages* msg)</td>
</tr>
<tr>
<td>(T_a) double TimeAdvanceFn()</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Table 3. Component API’s for Coupled Model</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Coupled Model</strong></td>
</tr>
<tr>
<td>Function</td>
</tr>
<tr>
<td>X AddInport([in] BSTR import)</td>
</tr>
<tr>
<td>Y AddOutport([in] BSTR output)</td>
</tr>
<tr>
<td>M AddChild([in] IUnknown* child)</td>
</tr>
<tr>
<td>EIC AddCoupling([in] IUnknown* model1, [in] BSTR port1, [in] IUnknown* model2, [in] BSTR port2)</td>
</tr>
<tr>
<td>EOC AddCoupling([in] IUnknown* model1, [in] BSTR port1, [in] IUnknown* model2, [in] BSTR port2)</td>
</tr>
<tr>
<td>IC AddCoupling([in] IUnknown* model1, [in] BSTR port1, [in] IUnknown* model2, [in] BSTR port2)</td>
</tr>
<tr>
<td>SELECT AddPriority([in] IUnknown* model);</td>
</tr>
</tbody>
</table>

Multi-Language Support

It is unrealistic to believe that any single programming language is best adequate for every task. For example, visual basic can hide sophisticated simulation model initialization code but results in inevitable performance degradation. Programming language independence on the COM framework may elevate the system modeling performance by adopting easy-to use program language such as java to implement the coupled model and performance-critical C++
language to an overall system bottleneck component. Figure 2 shows how the system interoperate with other model in a different language.

![component DEVS simulator](image)

**Figure 2. Cross Language Interoperability**

Some comments about the rightmost model named Web are followed. Microsoft proposed some means of interoperability between component on the web server and client. This implies that a web-based simulation client can use model components without any modification on component even though it is not possible to make a new model component on the worldwide web.

**COMPONENT-BASED MODELING/SIMULATION IN DEVS/COM**

In this section we discuss how to use component-based simulation framework to do a discrete event simulation by an example and the benefits of the component-based environment.

To verify the proposed framework, a system of cache memory model is offered and comparison of input/output traces between DEVS/COM and existing C++-based DEVS simulator, DEVSim++ [7] validates the correctness. Effectiveness measures between DEVS/COM and DEVSim++ is also shown to compare performance of these simulators. The procedures are shown on figure 3.

![Figure 3. Validation and Verification Procedure](image)

**Cache System**

System is configured with a CPU, a cache memory, a main memory, and an associated memory controller. The system objective is to undertake a performance evaluation with various cache sizes, cache block sizes, memory sizes, word sizes and CPU cycle time. Overall cache model is shown on figure 4. With this example, the input/output comparison and performance evaluation is done between DEVS/COM and DEVSim++ simulator.

![Figure 4. Cache System Coupled Model](image)

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![Figure 4. Cache System Coupled Model](image)
Characteristic functions of DEVS atomic model are checked on figure 5. Sequential state set on DEVS atomic model corresponds to state variable. Input/output event set are implemented as addinputs/addoutputs function. Characteristic functions of DEVS atomic model are need to be implemented on DEVS/COM atomic component by user but only external transition function is shown on figure 5.

Simulation and Analysis

To check for the correctness, several input sets with various scenarios are tested and input/output traces between DEVS/COM and DEVSim++ are compared. The results are shown on table 4.

<table>
<thead>
<tr>
<th>Input Type</th>
<th>Size</th>
<th>Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>gcc.strace</td>
<td>9264kB</td>
<td>1000002 memory references</td>
</tr>
<tr>
<td>tex.strace</td>
<td>7776kB</td>
<td>832476 memory references</td>
</tr>
<tr>
<td>spice.strace</td>
<td>9214kB</td>
<td>1000001 memory references</td>
</tr>
</tbody>
</table>

Table 4. Simulation Results (Correctness)

The first instruction takes 18 cycles and the second one takes 34 as writeback operation. On the associativity 2, the second instruction does not need to perform a writeback operation and the last instruction can be read from the existing cache and so the last operation takes only one cycle.

For performance evaluation, following input vectors are used as an input instruction. Each of input traces are pseudo code of real-world application of C compiler, tex document formatting program, and spice simulation program in a specific format, diagram format[8]. Input vectors are listed on table 5 and the simulator execution performance comparison between DEVS/COM execution and DEVSim++ is shown on table 6.

<table>
<thead>
<tr>
<th>Input Type</th>
<th>Characteristic</th>
</tr>
</thead>
<tbody>
<tr>
<td>gcc.strace</td>
<td>5 min 4 sec</td>
</tr>
<tr>
<td>tex.strace</td>
<td>2 min 28 sec</td>
</tr>
<tr>
<td>spice.strace</td>
<td>4 min 8 sec</td>
</tr>
</tbody>
</table>

Table 5. Input Instruction Sets

CONCLUSION AND FUTURE WORK

This paper presented a simulation environment for discrete event modeling and simulation using COM component technology. By employing DEVS/COM runtime environment, the following benefits can be expected. First, COM technology is an industry standards enabling integration of the modeling in the multi-language. Second, various system architectures can be a ready-to-deploy component that a user may re-use it. And component framework makes a model component independent with other component so possible problem in one component does not propagate into other model and each model can be distributed in a binary level without recompiling or linking by end user.

A component approach brings many benefits to the DEVS framework with some side effects. One is an interface overhead for a component and the other is performance degradation. DEVS/COM execution framework is about 10 times slower than non-component simulation environment. And both knowledge on DEVS formalism and component framework that are needed to build a robust component-based simulation environment yields other intrinsic problem is its implementation complexity. The most acceptable solution is to develop a graphical user interface to aid a modeler to make a model and to automate complicate but repeated component model implication. As component can be used a unit of deployment, this approach can be supported by component framework itself.

Like other network-oriented component technologies, COM component framework evolves to the DCOM (distributed COM) and COM+ to meet the current trends on the Internet. The model and simulator component may be easily used in the network environment with least modification. The comparison of distributed DEVS/COM with DEVS/COM and DEVS/HLA [9] (High Level Architecture) environment will be need on that time.

REFERENCES


BIOGRAPHY

YOUNG IK CHO received his M. S degree in the Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea.

TAG GON KIM received his Ph.D. in computer engineering with specialization in methodology for systems modeling/simulation from University of Arizona, Tucson, AZ, 1988. He joined at Electrical Engineering Department of KAIST, Tajeon, Korea in Fall, 1991 as an Assistant Professor and has been a Full Professor since Fall, 1998. His research interests include methodological aspects of systems modeling simulation, analysis of computer/communication networks, and development of simulation environments. He has published more than 100 papers on systems modeling, simulation and analysis in international journals/conference proceedings. He is a co-author (with B.P. Zeigler and H. Praehofer) of Theory of Modeling and Simulation (2nd ed.), Academic Press, 2000. He is the Editor-in-Chief of Transactions of Society for Computer Simulation. He is a senior member of IEEE and SCS and a member of ACM and Eta Kappa Nu.