

1. Objective

- Design and implementation of the simulation model proposed in Project #1.

2. Report should include :

- Description on the problem (reuse from proposed)
 - ① Brief description on the problem
 - ② System architecture diagram

- Model of the System
 - ① Decompose an overall system, consisting of a system model and an experimental frame, in a hierarchical form using a tree called the decomposition tree (DT)
 - A. Each node represents a name of a model component.
 - B. Each non-leaf node should have three coupling schemes in a form of (A.out, B.in) :
 - i. Between a parent node and children nodes : EIC (External Input Coupling) and EOC (External Output Coupling)
 - ii. Between children nodes : IC (Internal Coupling)
 - ② Map the DT to DEVS models: leaf nodes to atomic DEVSs and inner nodes to coupled DEVSs.
 - ③ DEVS formalization of each model.

- Design of an experimental frame
 - ① Input data preparation (reuse)
 - ② Performance index and measurement methods (reuse)

- DEVSim++ implementation of the system model and the experimental frame

- Construct a table for verification
 - ① Verification of each atomic model with the following format :

input event/time	Current discrete state	next discrete state	output event/time

- ② Verification of each coupled model including experimental frame with the following format :

input event/time	output event/time

- ③ Verification of the overall model excluding experimental frame with the following format :

input event/time of overall model	output event/time of overall model

- Verify your implementation using the table and monitoring messages and states of DEVS model components

Due on Dec. 3.

Submit : E-mail (kbgmode@kaist.ac.kr)

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